Application No. 09/750,093
Amendment dated September 22, 2005
Office Action dated March 21, 2005

REMARKS

Claims 18-28 are pending in this application. The Office Action rejects claims 1, 4-10, and 13-15, and 18-28 under 35 U.S.C. § 102(b) as being anticipated by Goss *et al.*, U.S. Patent No. 3,909,797 (hereinafter "*Goss*"). Claims 1-17 are herby cancelled without prejudice or disclaimer to the subject matter disclosed therein. Claims 18-26 are amended to clarify the scope of the invention. Claims 27 and 28 remain as previously presented. Applicants note that the Office Action, mailed March 21, 2005, omits an indication that claims 27 and 28 (added by amendment filed on November 12, 2004) are pending in the application. Applicant believes this is a typographical error and requests clarification in the next communication from the Office.

Applicants respectfully request the Office to confirm that a change to the title of the pending application has been entered into the record of the USPTO. The change of title was filed on November 10, 2004, per the request of the Office. Lastly, Applicants greatly thank Examiner Meonske for the courtesies extended to Applicant's representative during the personal interview held on April 19, 2005.

Applicants respectfully submit that Goss does not teach, suggest or disclose "[a] microinstruction sequencer, comprising: a microinstruction sequencer stack comprising an array of memory cells; and microinstruction sequencing logic associated with the microinstruction sequencer stack, wherein the microinstruction sequencing logic is responsive to *an operation code that includes a first field for non-microinstruction sequencer stack operations and a second field for microinstruction sequencer stack operations*" (e.g., as disclosed in the embodiment of claim 18).

Applicants note that the Office asserts that independent claims 18 and 19 do not recite limitations above the claimed invention set forth in claims 1 and 13, respectively, and were therefore rejected on the same grounds as claims 1 and 13, respectively (paragraphs 14 and 15 of the recent Office Action). Applicants point out that claims 1 and 13 do not recite "an operation code that includes a first field for non-microinstruction sequencer stack operations and a second field for microinstruction sequencer stack operations," as recited in independent claims 18 and 19, and the rejection of claims 1 and 13 do not address at least these limitations as well. Thus, Applicant's disagree with the Office's comments in paragraphs 14 and 15 of the Office Action.

-5-

Application No. 09/750,093
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Moreover, Applicants assert that *Goss* fails to disclose "an operation code that includes a first field for non-microinstruction sequencer stack operations and a second field for microinstruction sequencer stack operations," as recited in independent claims 18 and 19.

Therefore, since each and every element of claim 18 is not taught, suggested or disclosed by the cited reference, Applicant respectfully submits that the 102(b) rejections are lacking and should be withdrawn. Likewise, independent claims 19 includes similar limitations. Claims 20-28 depend from and further define claims 18 and 19, and should be allowable for the same reasons.
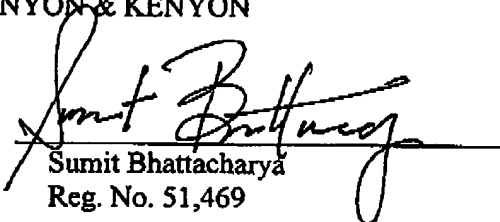
The Office is hereby authorized to charge all required fees, including all required claim fees under 37 C.F.R. §1.16 and/or all required extension of time fees under 37 C.F.R. §1.17, or credit any overpayments to Deposit Account 11-0600.

Respectfully submitted,

KENYON & KENYON

Date: September 22, 2005

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-6-